

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,813,214 B2  
DATED : November 2, 2004  
INVENTOR(S) : Cho et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7.

Line 30, replace "transistors M1\_O, M2\_, M3\_O" with -- transistors M1\_O, M2\_O, M3\_O --.

Line 31, replace "transistor M6\_," with -- transistor M6\_O --.

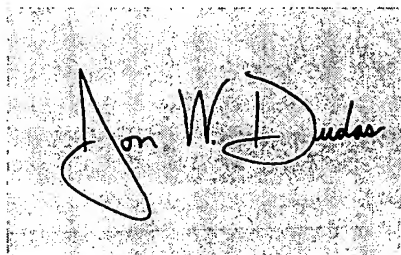
Line 34, replace "transistors M1\_, M2\_O" with -- transistors M1\_O, M2\_O --.

Column 13.

Line 43, replace "transistors MS\_E" with -- transistors M8\_E --.

Signed and Sealed this

Twenty-seventh Day of December, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a rectangular area with a light, textured background.

JON W. DUDAS

*Director of the United States Patent and Trademark Office*